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1	resist$7 and diode and nano$9
2	resist$7 and diode and nano$9 and @ad<"20031023"
3	resist$7 and diode and nano$9 and @ad<"20031023" and foward$4 near3 bias$4
4	resist$7 and diode and nano$9 and @ad<"20031023" and foward$4 near3 bias$4 and revers$4 near3 bias$4
5	resist$7 and diode and nano$9 and @ad<"20031023" and (alternate ac) with current with source
6	resist$7 and diode and nano$9 and @ad<"20031023" and (alternate ac) with current with source and (and-gate or-gate nand-gate)
7	resist$7 and diode and nano$9 and @ad<"20031023" and and-gate and or-gate and nand-gate
8	alternate adj current and direct adj current and @ad<"20031023"
9	alternate adj current and direct adj current and @ad<"20031023" and diode and resist$4
10	alternate adj current and direct adj current and @ad<"20031023" and diode and resist$4 and ac adj2 (logic$4 state)
11	nano$7 and "716"/$.ccls.
12	nano$7 with gate and (ac same dc)
13	nano$7 with gate and (ac same dc) and @ad<"20031023" and impedance
14	nano$7 with gate and (ac same dc) and @ad<"20031023" and impedance and (ac alternate) with current
15	nano$7 with gate and ((ac same dc) or (direct same alternate)) same superimpos$5 and @ad<"20031023" and impedance same (switch$4 diode)
16	nano$7 with gate and ((ac same dc) or (direct same alternate)) and @ad<"20031023" and impedance same (switch$4 diode) and (ac alternate) with current
17	nano$7 with gate and ((ac same dc) or (direct same alternate)) and @ad<"20031023" and (impedance or (clos$4 same open$5)) same (switch$4 diode) and (ac alternate) with current
18	nano$7 with gate and (ac alternate) same current and @ad<"20031023"
19	nano$7 with gate and (ac alternate) same current and @ad<"20031023" and (impedance or (clos$4 same open$5)) same (switch$4 diode)
20	nano$7 with gate and (ac alternate) same current and @ad<"20031023" and (impedance or (clos$4 same open$5)) same (switch$4 diode) and resist$5
21	"20050091624" and ac same logic$5 same state and ac same logic$5 same output
22	(ac alternat$4) with (direct$4 dc) with superimpos$5 and @ad< "20031023"
23	(ac alternat$4) with (direct$4 dc) with superimpos$5 and @ad< "20031023" and nano$8 with gate
24	(nano adj wire nanowire) and "716"/$.ccls.
25	(nano adj wire nanowire) and "716"/$.ccls.
26	(nanosale nano adj scale nanotech$7 nano adj tech$7) and "716"/$.ccls.
27	(nanosale nano adj scale nanotech$7 nano adj tech$7) and "716"/$.ccls. and (ac alternat$4) with (direct$4 dc) with superimpos$5
28	(nanosale nano adj scale nanotech$7 nano adj tech$7) and "716"/$.ccls. and (ac alternat$4) with (direct$4 dc) with (superimpos$5 super adj impos$5)

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29	(nanosale nano adj scale nanotech$7 nano adj tech$7 nano adj wire nanowire) and "716"/$.ccls. and (ac alternat$4) with (direct$4 dc) with (superimpos$5 super adj impos$5)
30	(ac alternat$4) same (direct$4 dc) same superimpos$5 and @ad< "20031023" and nano$8
31	(ac alternat$4) same (direct$4 dc) and superimpos$5 and @ad< "20031023" and nano$8
32	(ac alternat$4) same (direct$4 dc) and superimpos$5 and @ad< "20031023" and nano$8 and gate
33	(ac alternat$4) same (direct$4 dc) and superimpos$5 and @ad< "20031023" and nano$8 and logic$4 near2 gate
34	(ac alternat$4) same (direct$4 dc) and superimpos$5 and @ad< "20031023" and nano$8 and logic$4 near2 gate and (switch$5 diode)
35	(ac alternat$4) same (direct$4 dc) and resist$5 and @ad< "20031023" and nano$8 and logic$4 near2 gate and (switch$5 diode)
36	(ac alternat$4) same (direct$4 dc) and resist$5 and superimpos$5 and @ad< "20031023" and nano$8 and logic$4 near2 gate and (switch$5 diode)